# PAID INTERNSHIP



## DEFENCE ELECTRONICS RESEARCH LABORATORY Defence Research and Development Organization(DRDO) Government of India, Ministry of Defence Chandayanagutta, Hyderabad-500 005

Defence Electronics Research and Laboratory (DLRL) is a premiere Research Laboratory under DRDO, Ministry of Defence. As part of internship scheme introduced by Government of India, this Laboratory is engaging internships in the following Disciplines from the students who are pursuing the final year B.E./B.Tech in the academic year 2025-2026:-

SN O	Discipline	Number of vacancies
1	Electronics and Communication Engineering(ECE)	40
2	Computer Science Engineering(CSE)	25
3	Mechanical Engineering(Mech. Engg)	10

Interested candidates may submit the application in the prescribed proforma alongwith recent referral/ sponsorship letter from respective college. The terms and conditions are as under **Terms of conditions:** 

- 1. The students pursuing full time B.E/B.Tech and in final year only are eligible for the internship.
- 2. Students with backlogs are not eligible.
- 3. The paid internship shall be applicable in case of six months duration only.
- Stipend of total Rs. 30,000 will be paid in two installments (Rs. 15,000 after completion of 3 months and Rs 15,000 after 6 months) subject to attendance of minimum 15 days in a month.
- 5. Other expenditure such as lodging, boarding, transport, medical etc., if any will be borne by the individual concerned during the course of internship.
- 6. Students who do not complete the 6 months internship are not eligible for stipend. No certificate will be issued.
- 7. Completion of the internship does not guarantee employment in DRDO.
- 8. After selection to the internship, the candidate is required to obtain police verification certificate and submit at the time of joining.
- 9. Selected candidates will be intimated through mail along with terms & conditions and guidelines of internship.

### **Selection process :**

- 1. The selection will be based on merit i.e based on % of marks obtained in 3 years.
- 2. If two or more candidates have same percentage of marks then date of birth will be considered for the selection and the candidate elder in age will be given preference.

# **Application last date and instructions :**

- 1. Candidates shall apply for internship in the prescribed form only. Application can be downloaded from www.drdo.gov.in/drdo/careers.
- 2. List of enclosures to be submitted as per application form.
- 3. Duly filled in application alongwith the enclosures may be scanned and forwarded to this laboratory through mail to <u>hrdc.dlrl@gov.in</u>
- 4. Upon selection, candidates need to submit the ink signed application alongwith copies of enclosures and police verification certificate in this Office at the time of joining.
- 5. Last date for submission of application is 10-07-2025.
- 6. Applications submitted after due date will not be considered.
- 7. Incomplete applications will be rejected and no further communication will be entertained.
- 8. Canvassing in any form will be a disqualification.

Place: Hyderabad Date: 23-06-2025 Sd/-Chairperson Internship selection Committee For Director,DLRL

### DEFENCE ELECTRONICS RESEARCH LABORATORY, HYDERABAD-05.



ISO 9001:2015

### APPLICATION FORM FOR PAID INTERNSHIP OF DRDO

1.	Student's Name	:		
2.	B.E/ B. Tech (ECE / CSE / Mech.)	:	ſ	-
3.	College & University	:		
4.	Aggregate <b>%</b> of Marks (till 3 <sup>rd</sup> year)	:		
5.	Contact No & email I.D	:		_
			Signature	9 O

Affix latest passport size photograph

Signature of the Student

List of documents to be enclosed 1. Aadhar Card 2. College ID 3. Bonafide certificate 4. Marks Sheets (till 3<sup>rd</sup> year)

#### REFERRAL/ SPONSORSHIP LETTER FROM RESPECTIVE COLLEGE

The above student is studying in our college and we are sponsoring him/ her for Internship at DLRL, Hyderabad. We will be responsible for his/her good conduct during the tenure of the Internship at DLRL and assure that he/she will follow the security norms and discipline of the Laboratory. We undertake the responsibility in case of any breach of security, as in vogue in DLRL.

Details of marks obtained by the student is as under:-

year	Month and Year of	% of marks	In case of CGPA			
	Passing		CGPA	Conversion	%of marks	
				Factor		
1 <sup>st</sup>						
2 <sup>nd</sup>						
3 <sup>rd</sup>						
3 years average marks %						

Name of Principal : Contact No: E mail address: Principal's Signature College Seal